

# **DOP-24**

**24 Channel Isolated  
Darlington Output Board**



**User Manual**



# **DOP-24**

## **User Manual**

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**Blue Chip Technology Ltd.**  
**Chowley Oak, Tattenhall**  
**Chester, Cheshire**  
**CH3 9EX.**  
**Telephone : 01829 772000 Facsimile : 01829 772001.**

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OUTLINE DESCRIPTION .....	1
IMPORTANT SAFETY NOTES .....	2
ELECTROMAGNETIC COMPATIBILITY (EMC).....	3
EMC Specification .....	4
1.0 SPECIFICATION.....	5
1.1 Electrical Specification .....	5
1.2 Physical Specification .....	5
2.0 USER ADJUSTMENTS .....	6
2.1 Selecting the Base Address.....	6
Figure 1 - Selecting the Base Address.....	7
2.2 Port Map .....	8
3.0 ELECTRICAL OPTIONS .....	9
3.1 Power Supply .....	9
3.2 Output Connections.....	9
Pin Detail .....	10
3.3 Connector Pin Details.....	11
4.0 OPERATING GUIDE.....	13
4.1 Using the Device.....	13
4.2 Programming Guide.....	14
4.3 Example Program .....	15
5.0 COMMERCIAL DATA ACQUISITION PACKAGES .....	16
5.1 Use of the DOP-24 Board with ASYST .....	16

Contents

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APPENDIX A .....	17
APPENDIX B .....	19
PC/XT/AT Port Map .....	19
I/O Address Map .....	19
APPENDIX C .....	20
PC/XT Interrupt Map .....	20
APPENDIX D .....	21
AT Interrupt Map .....	21

## **OUTLINE DESCRIPTION**

This card provides 24 opto-isolated Darlington outputs. It is suitable for driving DC loads in the range 5-24V at currents of up to 400mA per channel subject to a limit of 2W per package (8 bits). An optional on-board power inverter ensures complete isolation of the open collector output devices from the PC power supply, making the board suitable for driving lamps, stepper motors, relays and solenoids etc.

## **IMPORTANT SAFETY NOTES**

### **Board Revision D**

This version of the DOP card has a particular type of output chip which will power up in a determined state. Using 74LS240's in positions IC17, IC18 and IC19 on power up the outputs will be "off". From the factory, the card is shipped with this configuration.

If you require to select a block of 8 bits to be "on" on power up then remove the appropriate IC and fit a 74LS244. If you have any difficulty in obtaining this device please contact Blue Chip Technology. Alternatively, specify the power up mode you require the next time you order your DOP-24 from us.

## ELECTROMAGNETIC COMPATIBILITY (EMC)

This product meets the requirements of the European EMC Directive (89/336/EEC) and is eligible to bear the CE mark.

It has been assessed operating in a Blue Chip Technology Icon industrial PC. However, because the board can be installed in a variety of computers, certain conditions have to be applied to ensure that the compatibility is maintained. It meets the requirements for an industrial environment (Class A product) subject to those conditions.

- The board must be installed in a computer system which provides screening suitable for the industrial environment.
- Any recommendations made by the computer system manufacturer/supplier must be complied with regarding earthing and the installation of boards.
- The board must be installed with the backplate securely screwed to the chassis of the computer to ensure good metal-to-metal (i.e. earth) contact.
- Most EMC problems are caused by the external cabling to the board. Boards fitted with IDC ribbon cable connectors on the metal mounting bracket require particularly careful installation of the external cabling. The cabling must be totally screened; the type of ribbon cable which is rolled to a round form with a braided wire screen is best. Standard ribbon cable will not be adequate unless it is contained wholly within the cabinetry housing the industrial PC. Keep the unscreened section as short as possible. The mounting bracket of the board includes a captive nut as an screen earth point. Connect the screen of the cable to this by the shortest possible wire.
- To ensure that the board meets the industrial radiated field immunity of 10 V/metre, the cable should also be fitted with a ferrite clamp on the external cable as close as possible to the connector. The preferred type is the Chomerics clip-on style, type H8FE-1004-AS.
- Ensure that the screen of the external cable is bonded to a good RF earth at the remote end of the cable.

- Cables which connect externally to boards at TTL levels should not exceed two metres in length. This restriction does not apply to opto-isolated boards.

Failure to observe these recommendations may invalidate the EMC compliance.

**Warning**

This is a Class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

## EMC Specification

A Blue Chip Technology Icon industrial PC fitted with this card meets the following specification:

Emissions: EN 55022:1995

Radiated	Class A
Conducted	Class A & B

Immunity: pr EN 50082-2:1991 incorporating:

Electrostatic Discharge	IEC 801-2:1984 Performance Criteria A
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Radio Frequency Susceptibility	IEC 801-3:1984 Performance Criteria A
--------------------------------	--

Fast Burst Transients	IEC 801-4:1988 Performance Criteria A
-----------------------	--

## 1.0 SPECIFICATION

### 1.1 Electrical Specification

Number of Outputs	24
Maximum Load Voltage	24V DC
Maximum Load Current	400mA DC
Maximum Port Dissipation	2W

### 1.2 Physical Specification

Height	107mm
Width	15mm
Depth	132mm

## 2.0 USER ADJUSTMENTS

### 2.1 Selecting the Base Address

The board may be located in any 62 pin slot in the PC motherboard but must be set up to appear at a specified position (or 'address') in the port map. Available positions are shown in the IBM-PC technical reference guide. However, for those who do not possess a copy of this document a good place is the location normally allocated to the prototyping card as supplied by IBM. This address is 300 Hex or 768 decimal.

**All Blue Chip Technology cards are preset to this address at the factory.** However, no two devices should be used while set to the same address since contention will occur and neither board will work. There is one exception to this rule. You may place one DIP-24 and one DOP-24 on the same address boundary as the DIP card is only enabled on read instructions and the DOP card is only enabled on write instructions. This is a useful facility if you are short of port map space on your machine.

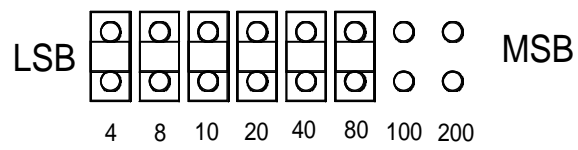
Links are provided on the board to set the base address of the board within the IBM-PC port map. The address is in binary with the presence of a link representing a 0 and the absence of a link representing a 1.

To set the base address to 768 Decimal (300 Hex) set the following pattern on the links as indicated below:-

## Figure 1 - Selecting the Base Address

Note: View board with back panel on RHS.

Middle 8 Bits of port address on links.



More example addresses are shown in Appendix A.

Note: No two cards must occupy the same address

## 2.2 Port Map

The DOP-24 board provides three 8 bit ports. Each bit can be independently driven via software to generate the required output. The card occupies four 8 bit ports although only three are used as shown below.

Address	Read	Write
Base + 0	N/A	Channels 0-7
Base + 1	N/A	Channels 8-15
Base + 2	N/A	Channels 16-23
Base + 3	Not used on card	

7 6 5 4 3 2 1 0



ADDRESS XX0H (W/O)

CHANNELS 0 TO 7  
(Base + 0)

(Bit 0 = Channel 0 etc.)

7 6 5 4 3 2 1 0



ADDRESS XX1H (W/O)

CHANNELS 8 TO 15  
(Base + 1)

(Bit 0 = Channel 8 etc.)

7 6 5 4 3 2 1 0



ADDRESS XX2H (W/O)

CHANNELS 16 TO 23  
(Base + 2)

(Bit 0 = Channel 16 etc.)

---

## **3.0 ELECTRICAL OPTIONS**

### **3.1 Power Supply**

If the inverter is fitted (IC1) then Diode D1 and resistor R5 should be removed from the board to isolate the external PSU from the internal one.

The positive power and ground applied to these pins should be from the same supply as the devices being driven with the output of the board. This will ensure that the back EMF catch diodes in the Darlington drivers are connected correctly.

The optional power inverter chip is available from Blue Chip Technology.

### **3.2 Output Connections**

A 50 way insulation displacement connector (IDC) is provided on the PC rear panel of the board for output signal connection. If access to individual channels is required, a 50 way IDC ribbon cable may be used to connect the outputs to a 50 way screw terminal block available from Blue Chip Technology as part number ST-24.

The pins are numbered as shown in the following diagram. Pins 1-48 contain the output channels and pins 49 and 50 are the common ground and positive connections respectively. It is company policy to call the even numbered pins positive and the negative on the DOP-24 to conform with the other cards in the range.

When the connector is viewed from the back of the system, odd numbered pins are on the left and even numbered pins are on the right with pin 1 at the top of the connector. It should be noted that the outputs of the DOP-24 board will not function correctly unless power is applied across pins 49 and 50 on the connector or the optional isolated power inverter is fitted to drive the internal opto-isolators.

**Pin Detail**

Pin	1	O	O	Pin	2
Pin	3	O	O	Pin	4
O		O	O	.	
.		O	O	.	
.				.	
.				.	
.		O	O	.	
.		O	O	.	
Pin	47	O	O	Pin	48
Pin	49	O	O	Pin	50

View with gold edge connectors downwards

### 3.3 Connector Pin Details

Pin Number	Port	Bit	Function
1	Base + 0	0	-ve Darlington drive
2	Base + 0	0	+ve common rail
3	Base + 0	1	-ve Darlington drive
4	Base + 0	1	+ve common rail
5	Base + 0	2	-ve Darlington drive
6	Base + 0	2	+ve common rail
7	Base + 0	3	-ve Darlington drive
8	Base + 0	3	+ve common rail
9	Base + 0	4	-ve Darlington drive
10	Base + 0	4	+ve common rail
11	Base + 0	5	-ve Darlington drive
12	Base + 0	5	+ve common rail
13	Base + 0	6	-ve Darlington drive
14	Base + 0	6	+ve common rail
15	Base + 0	7	-ve Darlington drive
16	Base + 0	7	+ve common rail
17	Base + 1	0	-ve Darlington drive
18	Base + 1	0	+ve common rail
19	Base + 1	1	-ve Darlington drive
20	Base + 1	1	+ve common rail
21	Base + 1	2	-ve Darlington drive
22	Base + 1	2	+ve common rail
23	Base + 1	3	-ve Darlington drive
24	Base + 1	3	+ve common rail
25	Base + 1	4	-ve Darlington drive
26	Base + 1	4	+ve common rail
27	Base + 1	5	-ve Darlington drive
28	Base + 1	5	+ve common rail
29	Base + 1	6	-ve Darlington drive
30	Base + 1	6	+ve common rail
31	Base + 1	7	-ve Darlington drive
32	Base + 1	7	+ve common rail
33	Base + 2	0	-ve Darlington drive
34	Base + 2	0	+ve common rail
35	Base + 2	1	-ve Darlington drive
36	Base + 2	1	+ve common rail
37	Base + 2	2	-ve Darlington drive
38	Base + 2	2	+ve common rail
39	Base + 2	3	-ve Darlington drive
40	Base + 2	3	+ve common rail

Pin Number	Port	Bit	Function
41	Base + 2	4	-ve Darlington drive
42	Base + 2	4	+ve common rail
43	Base + 2	5	-ve Darlington drive
44	Base + 2	5	+ve common rail
45	Base + 2	6	-ve Darlington drive
46	Base + 2	6	+ve common rail
47	Base + 2	7	-ve Darlington drive
48	Base + 2	7	+ve Common rail
49			-ve DC GROUND INPUT
50			+ve DC POWER INPUT

## 4.0 OPERATING GUIDE

### 4.1 Using the Device

A total of 24 opto-isolated outputs may be driven by the DOP-24 board. These appear as three 8 bit ports. Each signal is connected to one bit within one of these ports i.e.

Ports	Bit	Hex	Decimal
	0	01	1
	1	02	2
	2	04	4
	3	08	8
Base + 0	4	10	16
	5	20	32
	6	40	64
	7	80	128
	0	01	1
	1	02	2
	2	04	4
	3	08	8
Base + 1	4	10	16
	5	20	32
	6	40	64
	7	80	128
	0	01	1
	1	02	2
	2	04	4
	3	08	8
Base + 2	4	10	16
	5	20	32
	6	40	64
	7	80	128

## 4.2 Programming Guide

The state of the output lines may be modified by using either of the following methods :-

- (a) Microsoft BASIC A or GW BASIC.

OUT P,D

Outputs the byte D to Port P.

- (b) 8088/8086 Assembly language.

PORT EQU 0300H

PUTDAT:

```
MOV  DX, PORT
MOV  AX, DATA
OUT  DX, AL
RET
```

### 4.3 Example Program

The following program written in Microsoft Basic will run under the BASIC A or GW BASIC interpreter and is intended to output a count of 0 to 255 to the outputs of the three DOP-24 ports on and off individually. This enables the user to see the output channels as they are activated.

The base port address used for the program is 300 Hex or 768 decimal which is the default factory setting for link A.

```
10    REM BLUE CHIP TECHNOLOGY DOP-24 TEST
      PROGRAM
20    REM D = DATA AND P = PORT NUMBER
30    FOR D=0 TOP 255
40    REM OUTPUT TO ALL THREE PORTS AT ONCE
50    FOR P=&H300 TO &H302
60    OUT P,D
70    NEXT
80    REM DELAY SO USER CAN SEE OUTPUT ON LAMPS
      ETC.
90    FOR T=1 TO 100:NEXT
100   NEXT
110   STOP
```

## **5.0 COMMERCIAL DATA ACQUISITION PACKAGES**

The Blue Chip Technology DOP-24 can be used with almost any data acquisition or control package that can write information directly to a PC output port.

### **5.1 Use of the DOP-24 Board with ASYST**

The board has been tested with and is installable as an IO.PORT digital device in the ASYST scientific software package by Macmillan Software Company.

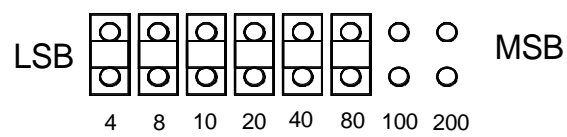
For more details about this package and other PC data acquisition software please contact Blue Chip Technology.

Whilst every effort has been taken to ensure that the information provided is accurate, Blue Chip Technology cannot assume responsibility for any errors in this manual or their consequences. Should any errors be detected, the company would greatly appreciate being informed of them. A policy of continuous product development is operated, resulting in the contents of this document being subject to change without notice.

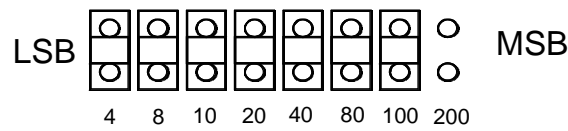
## APPENDIX A

Note: View board with back panel on RHS.

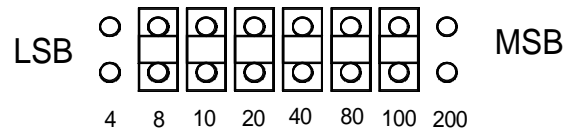
### Address Settings for Port 300H



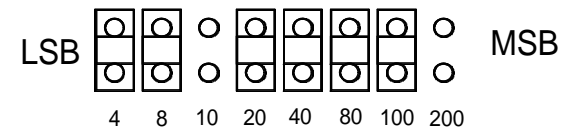
### Address Settings for Port 200H



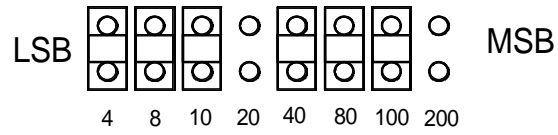
### Address Settings for Port 204H



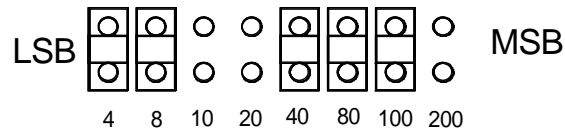
### Address Settings for Port 210H



**Address Settings for Port 220H**



**Address Settings for Port 230H**



## APPENDIX B

### PC/XT/AT Port Map I/O Address Map

Address	
000-01F	DMA Controller 1, 8237A-5
020-03F	Interrupt Controller 1, 8259A
040-05F	Timer, 8254
060-06F	Keyboard Controller, 8742; Control Port B
070-07F	RTC and CMOS RAM, NMI Mask (Write)
080-09F	DMA Page Register (Memory Mapper)
0A0-0BF	Interrupt Controller 2, 8259
0F0	Clear NPX (80287) Busy
0F1	Reset NPX, 80287
0F8-0FF	Numeric Processor Extension, 80287
1F0-1F8	Hard Disk Drive Controller
200-207	Reserved
278-27F	Reserved for Parallel Printer Port 2
2F8-2FF	Reserved for Serial Port 2
300-31F	Reserved
360-36F	Reserved
378-37F	Parallel Printer Port 1
380-38F	Reserved for SDLC Communications, Bisynchronous 2
3A0-3AF	Reserved for Bisynchronous 1
3B0-3BF	Reserved
3C0-3CF	Reserved
3D0-3DF	Display Controller
3F0-3F7	Diskette Drive Controller
3F8-3FF	Serial Port 1

**APPENDIX C****PC/XT Interrupt Map**

Number	Usage
NMI	Parity
0	Timer
1	Keyboard
2	Reserved
3	Asynchronous Communications (Secondary)
	SDLC Communications
4	Asynchronous Communications (Primary)
	SDLC Communications
5	Fixed Disk
6	Diskette
7	Parallel Printer

## APPENDIX D

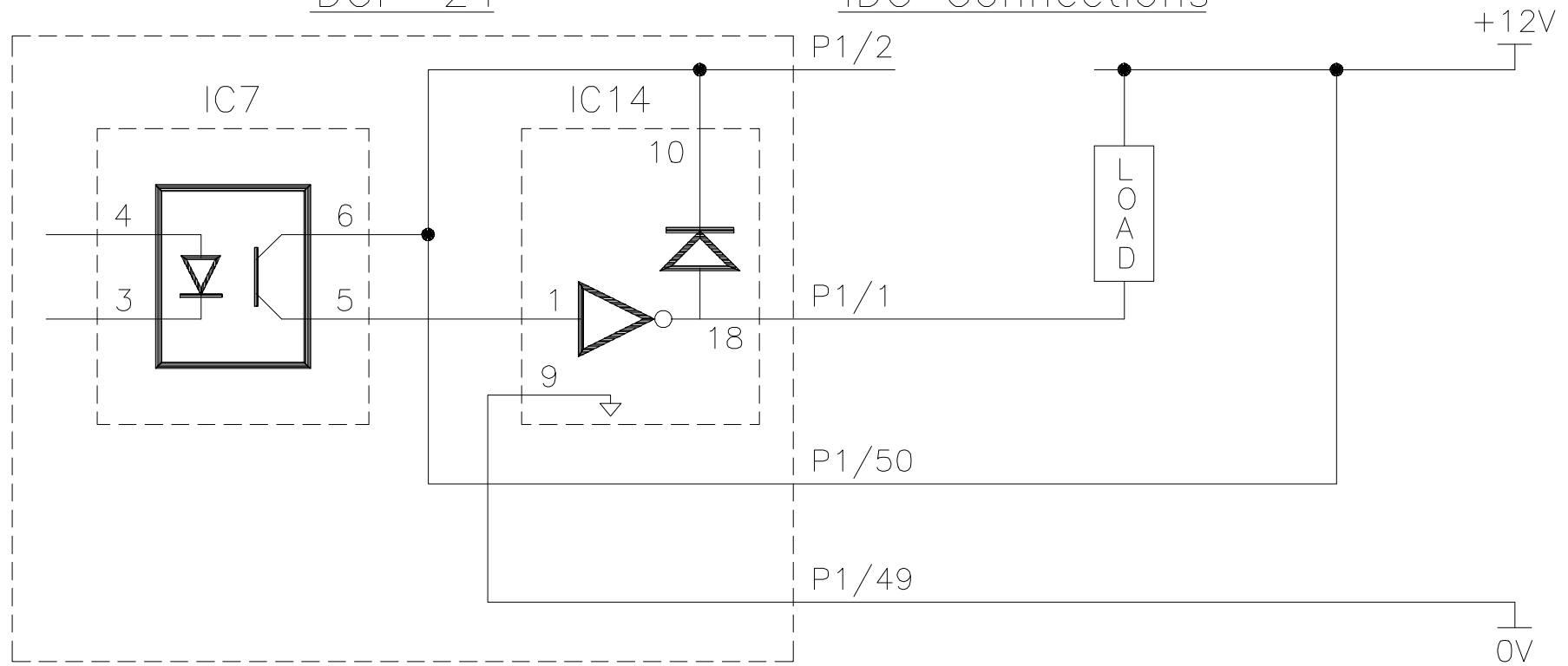
### AT Interrupt Map

Level	Function
Microprocessor NMI Parity or I/O Channel Check	
Interrupt Controllers	
CTLR 1	CTLR2
IRQ 0	Timer Output 0
IRQ 1	Keyboard (Output Buffer Full)
IRQ 2	Interrupt from CTLR 2
	IRQ 8
	IRQ 9
	IRQ 10
	IRQ 11
	IRQ 12
	IRQ 13
	IRQ 14
	IRQ 15
IRQ 3	Serial Port 2
IRQ 4	Serial Port 1
IRQ 5	Parallel Port 2
IRQ 6	Diskette Controller
IRQ 7	Parallel Port 1

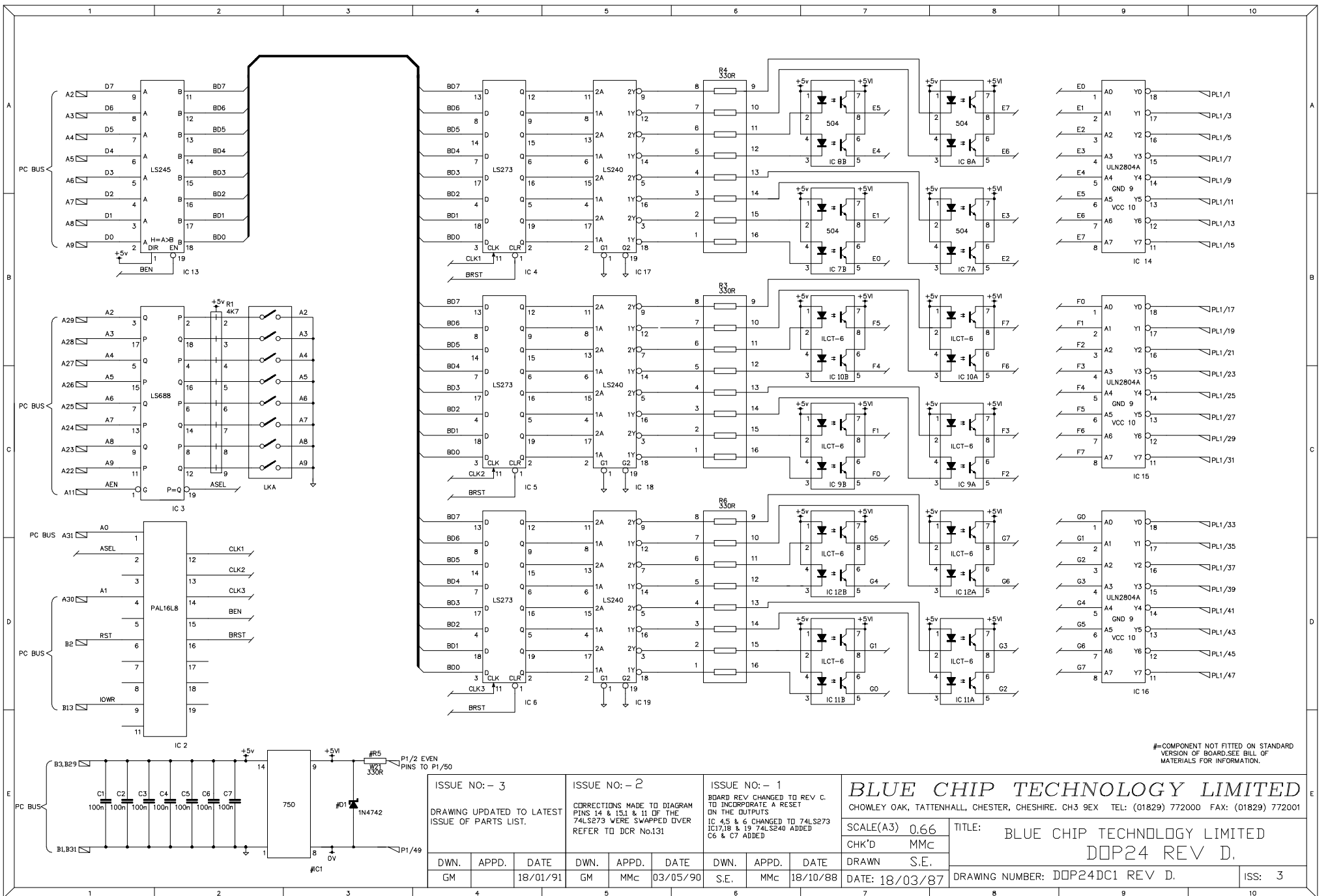
# A Typical Connection To A DOP-24

DOP-24

IDC Connections



(NB: ALL LINKS VIEWED WITH CONNECTORS ON RHS)



#=COMPONENT NOT FITTED ON STANDARD VERSION OF BOARD. SEE BILL OF MATERIALS FOR INFORMATION.

ISSUE NO: - 3			ISSUE NO: - 2			ISSUE NO: - 1		
DRAWING UPDATED TO LATEST ISSUE OF PARTS LIST.			CORRECTIONS MADE TO DIAGRAM PINS 14 & 15 & 11 OF THE 74LS273 WERE SWAPPED OVER REFER TO DCR No.131			BOARD REV CHANGED TO REV C. TO INCORPORATE A RESET ON THE OUTPUTS		
DWN.	APPD.	DATE	DWN.	APPD.	DATE	DWN.	APPD.	DATE
GM		18/01/91	GM	MMC	03/05/90	S.E.	MMC	18/10/88

**BLUE CHIP TECHNOLOGY LIMITED**  
 CHOWLEY OAK, TATTENHALL, CHESTER, CHESHIRE. CH3 9EX TEL: (01829) 772000 FAX: (01829) 772001

SCALE(A3) 0.66  
 CHK'D MMC  
 DRAWN S.E.  
 DATE: 18/03/87

TITLE: BLUE CHIP TECHNOLOGY LIMITED  
 DOP24 REV D.

DRAWING NUMBER: DOP24DC1 REV D. ISS: 3

